

AUG 1 4 2005

CLAIMS

Claims 21-33 (cancelled).

I claim:

- 34. (new) A device comprising:
 - (a) a non flat surface shape;
 - (b) icons arranged in a periodic and/or irregular pattern;
 - (c) emitters and/or receptors.
- 35. (new) A device as claimed in claim 34:

That emits and/or receives signals comprising at least one of the following group or a mixture:

- (a) electromagnetic radiation including light;
- (b) vibrations including sound;
- (c) particles including positrons;
- 36. (new) A device as claimed in claim 35: where a 2 or 3 dimensional image is displayed and/or recorded.
- 37. (new) A device as claimed in claim 36 where at least one icon moves from a first position to a second position in the process of emitting and/or receiving a signal.

38. (new) A camouflage device according to claim 36 in reference to which icons display a 3 dimensional image so as to make the device appear on at least part of its surface:

at least one of the following group or a mixture:

- (a) partially transparent;
- (b) invisible;
- (c) mimicking the appearance of a different object.
- 39. (new) A device according to claim 36, a three dimensional irregular surface used as a data storage medium containing a plurality of icons.
- 40. (new) A device as claimed in claim 39 where alterations to the storage surface comprising at least one from the following group:
 - (a) shape;
 - (b) electrical charge;
 - (c) magnetized areas,

are read by a first viewer in a first position and a second viewer in a second position.

- 41. (new) A device where a plurality of tubes direct at least one from the following group or a mixture thereof:
 - (a) electromagnetic radiation including light;

- (b) vibrations including sound;
- (c) materials including liquids;

in operations between nodes comprising at least one logic circuit.

- 42. (new) A device according to claim 41 where facets comprise gates represented in Boolean algebra.
- 43. (new) A device as claimed in claim 42, comprising:
 - (a) a means to synchronize operations including a computer clock;
 - (b) at least one connection to a memory storage device.
- 44. (new) A device according to claim 43 which creates at least one custom logic circuit in real time.
- 45. (new) A device according to claim 44 where data is stored in a 3 dimensional array comprising nodes and tubes.